

Notice of References Cited	Application/Control No. 10/707,388	Applicant(s)/Patent Under Reexamination DIVAKARUNI ET AL.	
	Examiner Steven J. Fulk	Art Unit 2891	Page 1 of 1

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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*	B	US-2003/0122175	07-2003	Buskirk, Peter Van	257/310
*	C	US-5,451,551	09-1995	Krishnan et al.	438/626
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	M	US-			

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
*	U	Wolf, "Silicon Processing for the VLSI Era: Process Integration," Vol. II, 1990, pp. 146, 176, 193.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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